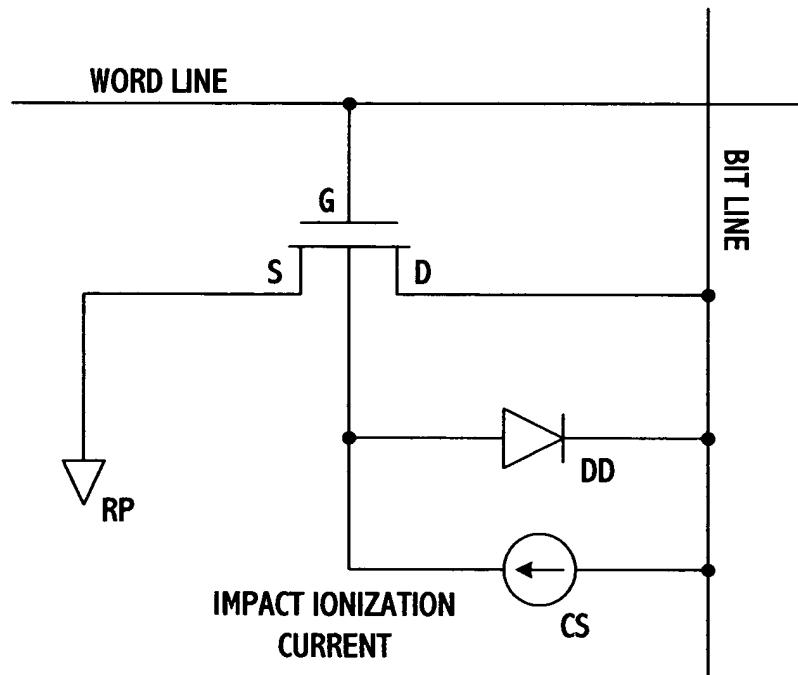
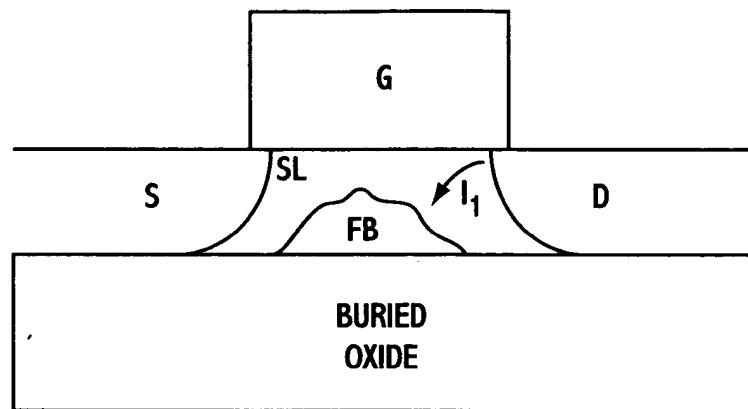


**FIG. 1**



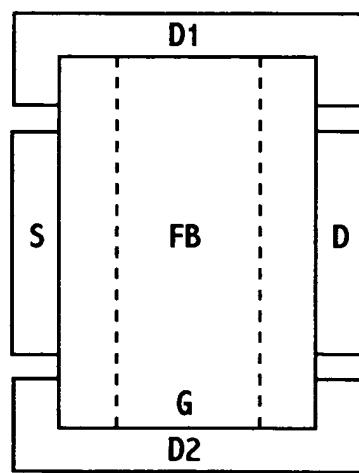
**FIG. 2**



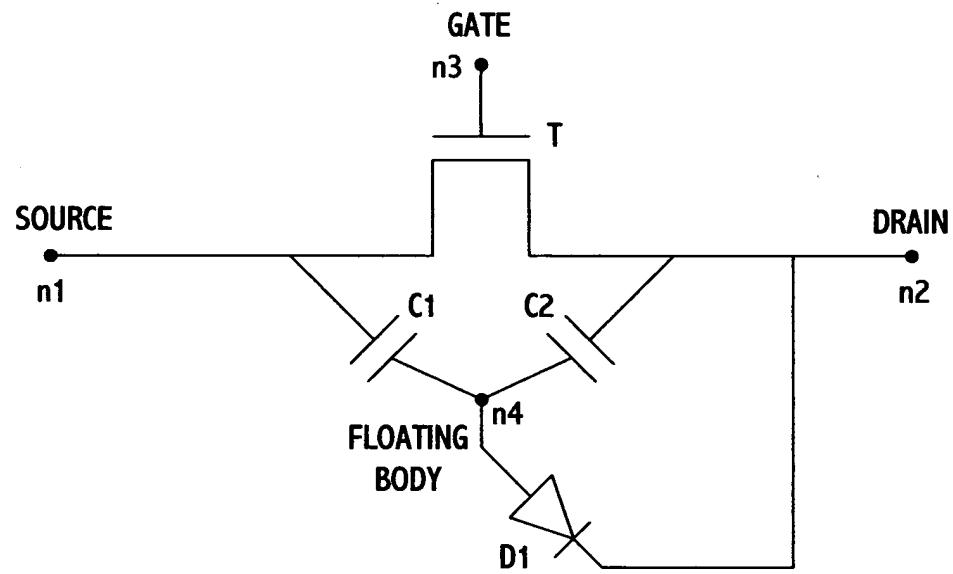
S

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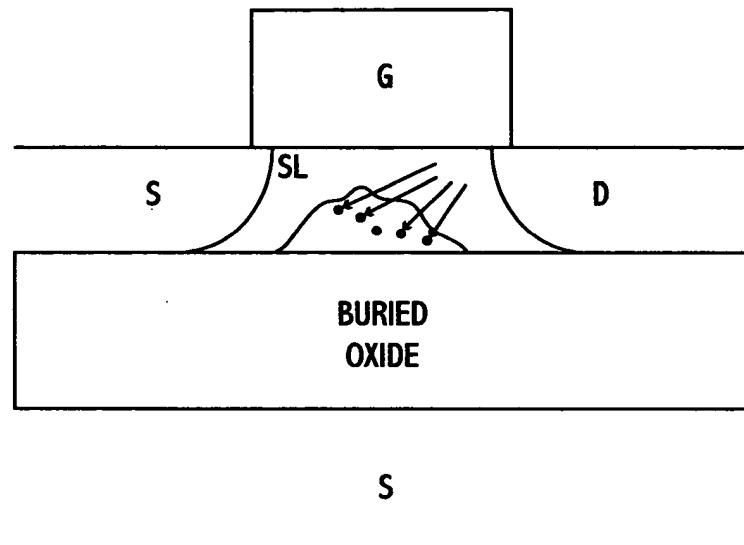
**FIG. 3**



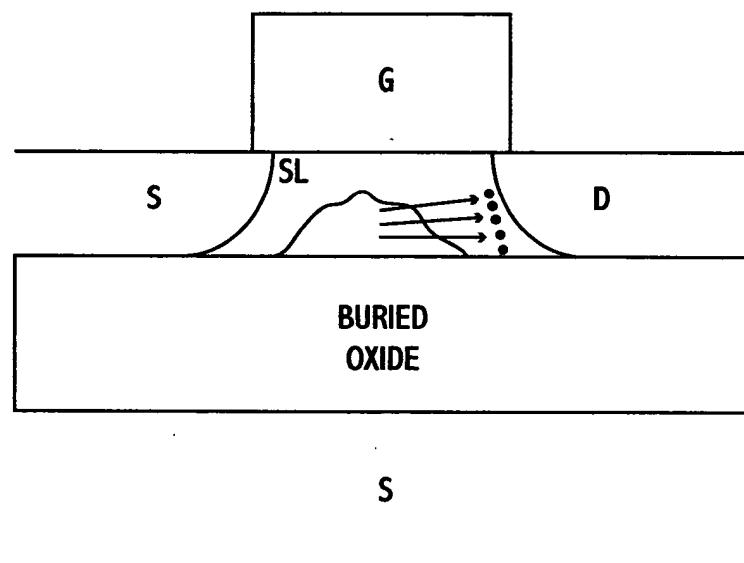
**FIG. 4**



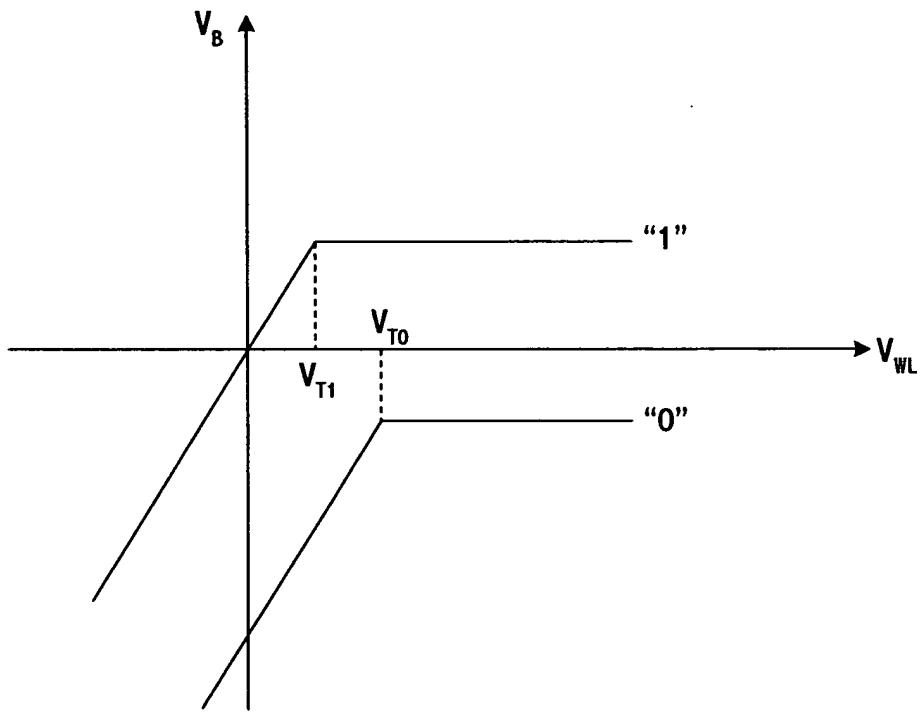
**FIG. 5**



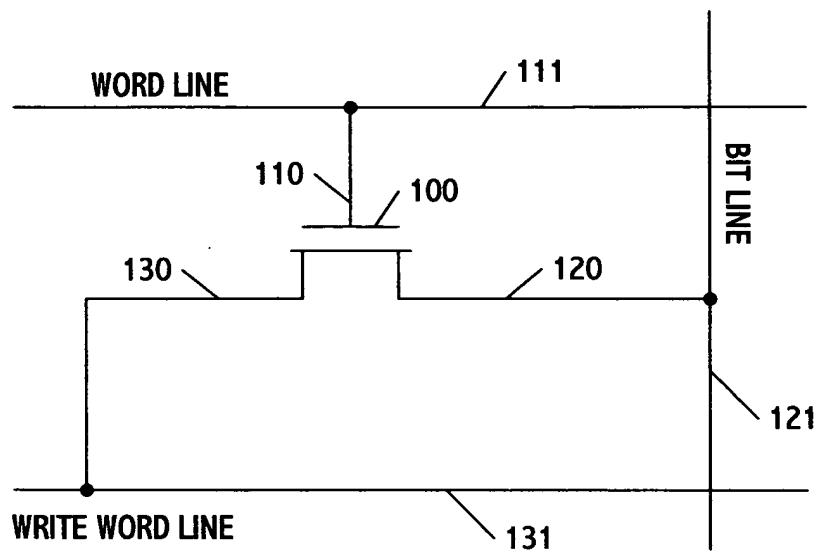
**FIG. 6A**



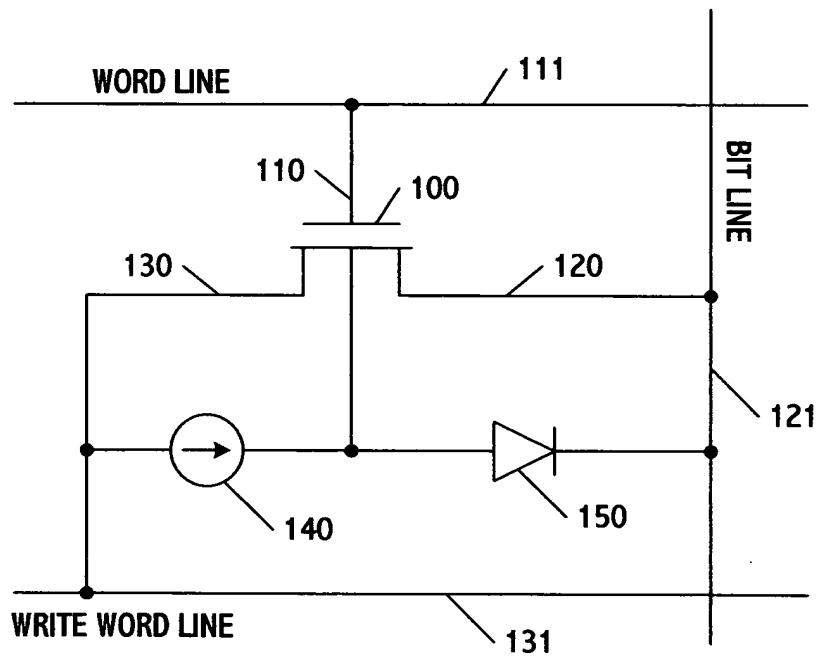
**FIG. 6B**



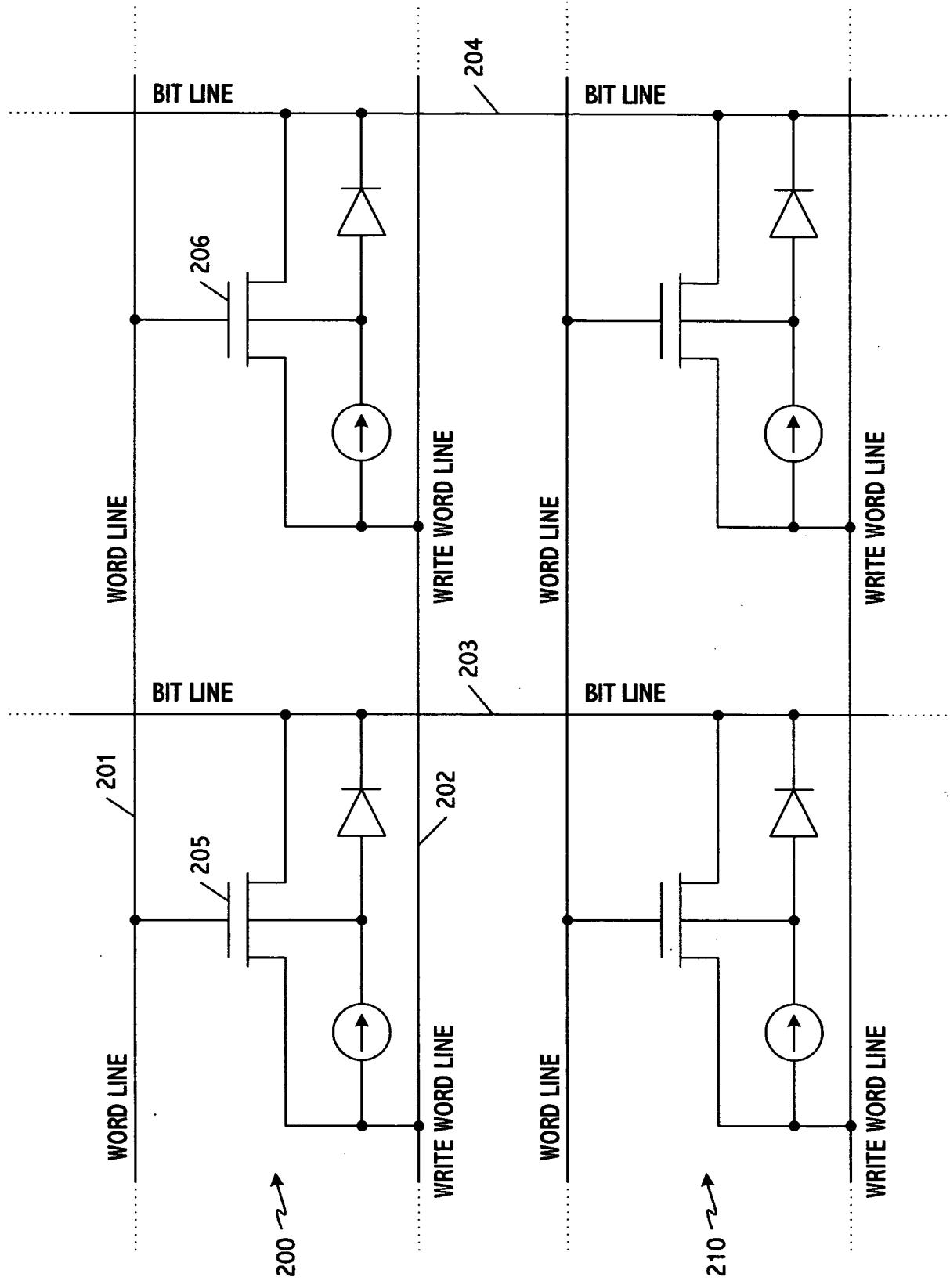
**FIG. 7**



**FIG. 8**



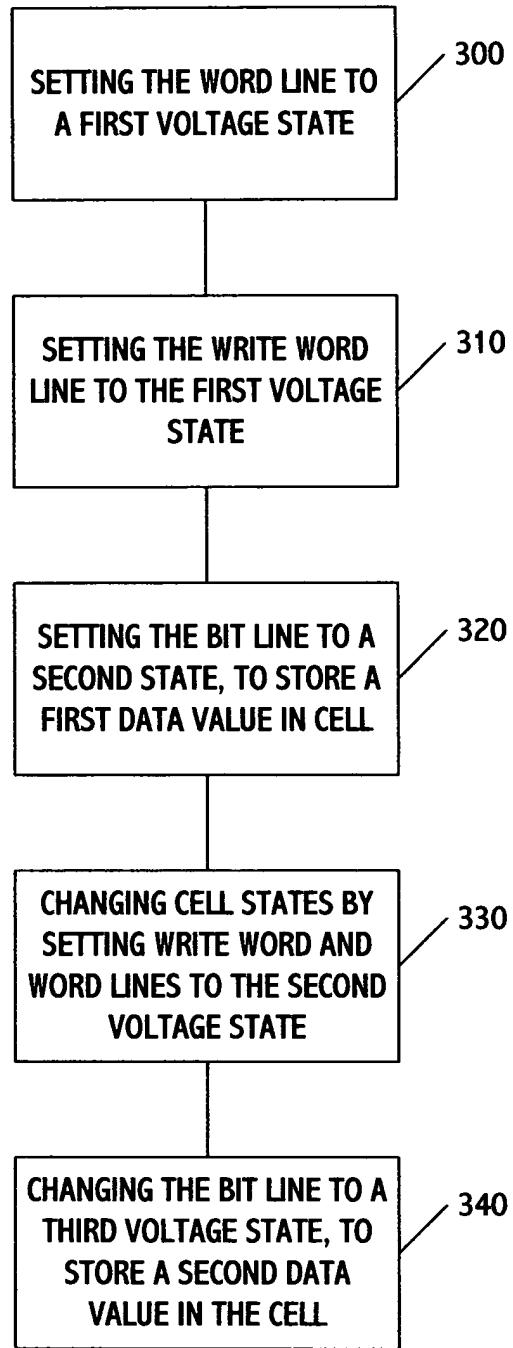
**FIG. 9**



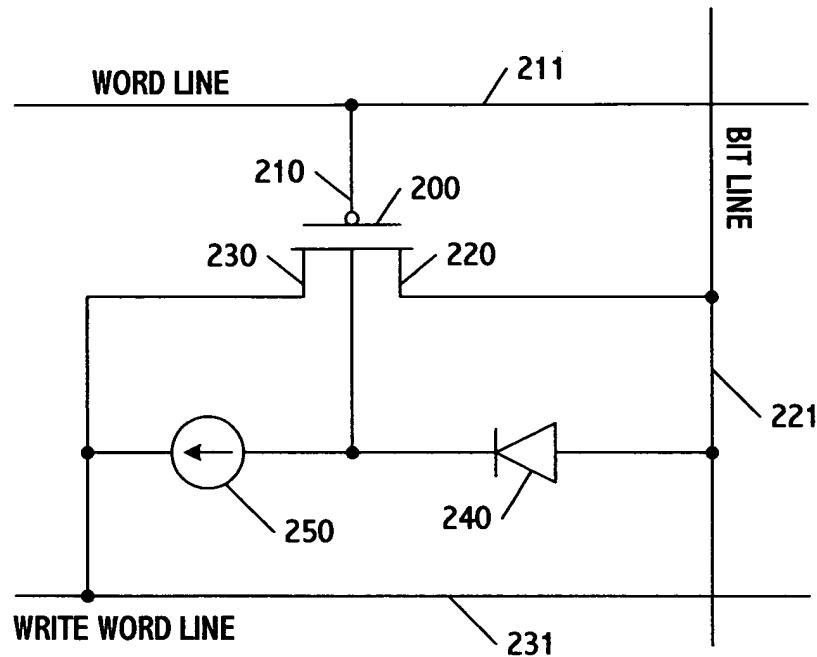
**FIG. 10**

	DRAIN (WRITE WORD LINE)	GATE (WORD LINE)	SOURCE (BIT LINE)
HOLD	LOW	VERY LOW (ACCUMULATED)	LOW
ROW WRITE 1 (IMP. ION. ON)	HIGH	HIGH	LOW
KEEP1/WRITE0 (DIODE OFF/ON)	LOW	MEDIUM TO LOW	LOW/VERY LOW

**FIG. 11**



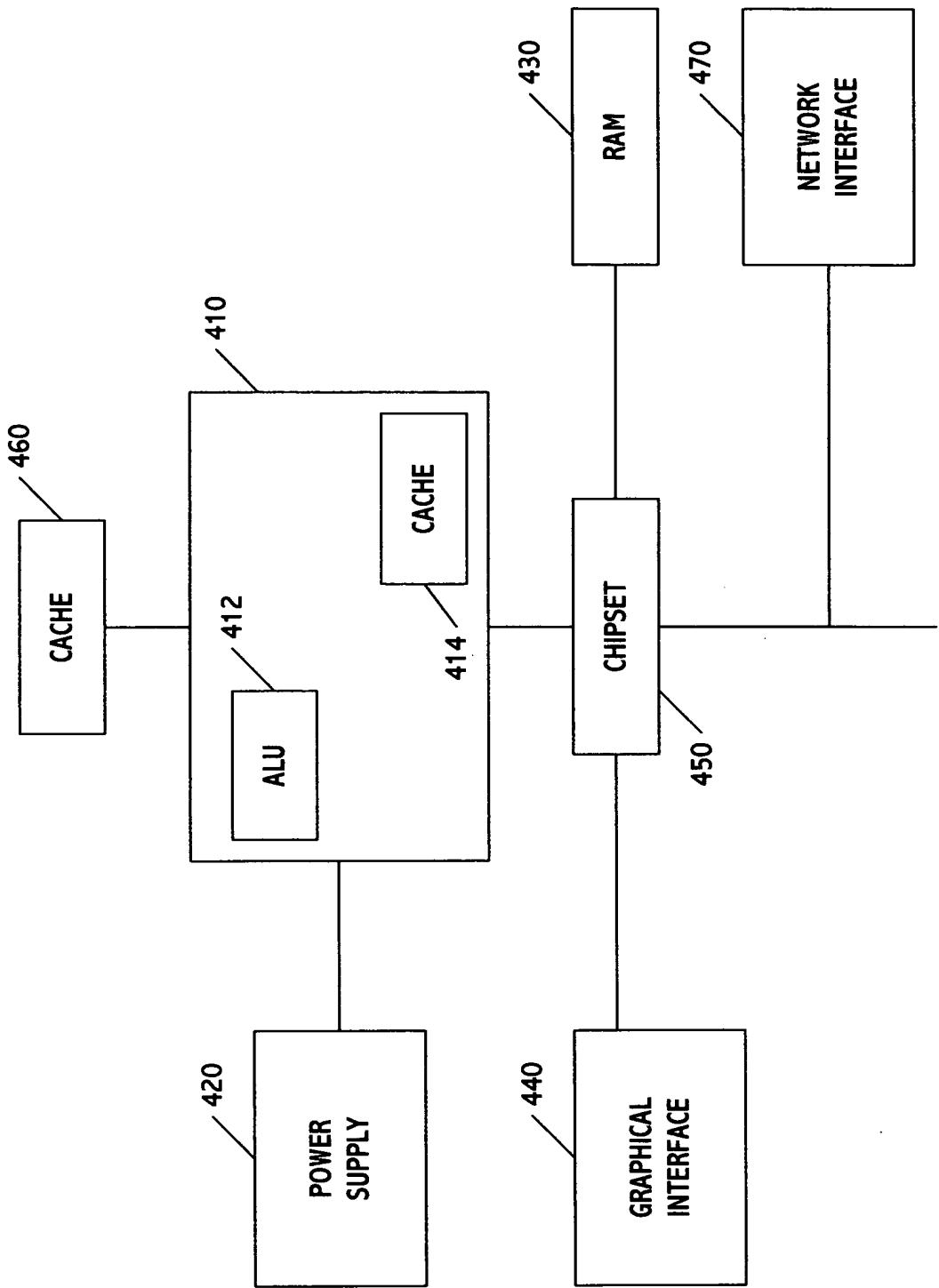
**FIG. 12**



**FIG. 13**

	DRAIN (WRITE WORD LINE)	GATE (WORD LINE)	SOURCE (BIT LINE)
HOLD	HIGH	VERY HIGH (ACCUMULATED)	HIGH
ROW WRITE 1 (IMP. ION. ON)	LOW	LOW	HIGH
KEEP1/WRITEO (DIODE OFF/ON)	HIGH	MEDIUM TO HIGH	HIGH/ VERY HIGH

**FIG. 14**



**FIG. 15**